

**SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER (COMMON TO EC/TC/ML)**

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 31	Engg. Mathematics - III	Mat	04		03	25	100	125
10ES – 32	Analog Electronic Ckts	@	04		03	25	100	125
10ES – 33	Logic Design	@	04		03	25	100	125
10ES – 34	Network Analysis	@	04		03	25	100	125
10IT– 35	Electronic Instrumentation	@	04		03	25	100	125
10ES – 36	Field Theory	@	04		03	25	100	125
10ESL – 37	Analog Electronics Lab	@		03	03	25	50	75
10ESL – 38	Logic Design Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

**SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
IV SEMESTER (COMMON TO EC/TC/ML)**

Subject Code	Title	Teach Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 41	Engg. Mathematics – IV	Mat	04		03	25	100	125
10ES- 42	Microcontrollers	@	04		03	25	100	125
10ES – 43	Control Systems	@	04		03	25	100	125
10EC – 44	Signals & Systems	@	04		03	25	100	125
10EC- 45	Fundamentals of HDL	@	04		03	25	100	125
10EC – 46	Linear ICs & Applications	@	04		03	25	100	125
10ESL – 47	Microcontrollers Lab	@		03	03	25	50	75
10ECL – 48	HDL Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : @ indicates concerned discipline. **ES (for theory) & ECL (for Lab)** in the subject code indicates that the subject is common to electrical and electronics stream consisting of **EE/EC/IT/TC/ML/BM branches** of engineering.

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

V SEMESTER

COMMON TO EC/TE

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
01	10AL51	Management and Entrepreneurship	AL	4	-	3	25	100	125
02	10EC52	Digital Signal Processing	EC	4	-	3	25	100	125
03	10EC53	Analog Communication	EC	4	-	3	25	100	125
04	10EC54	Microwaves and Radar	EC	4	-	3	25	100	125
05	10EC55	Information Theory & Coding	EC	4	-	3	25	100	125
06	10EC56	Fundamentals of CMOS VLSI	EC	4	-	3	25	100	125
07	10ECL57	DSP Lab	EC	-	3	3	25	50	75
08	10ECL58	Analog Communication Lab + LIC Lab	EC	-	3	3	25	50	75
TOTAL				24	06	24	200	700	900

**SCHEME OF TEACHING AND EXAMINATION
B.E. ELECTRONICS AND COMMUNICATION**

VI SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC61	Digital Communication	EC	4	-	3	25	100	125
2	10EC62	Microprocessors	EC	4	-	3	25	100	125
3	10EC63	Microelectronics Circuits	EC	4	-	3	25	100	125
4	10EC64	Antennas and Propagation	EC	4	-	3	25	100	125
5	10EC65	Operating Systems	EC	4	-	3	25	100	125
6	10EC66x	Elective-I (Group A)	EC	4	-	3	25	100	125
7	10ECL67	Advanced Communication Lab	EC	-	3	3	25	50	75
8	10ECL68	Microprocessor Lab	EC	-	3	3	25	50	75
TOTAL				24	06	24	200	700	900

Elective-I (Group A)

10EC661 – Analog and Mixed Mode VLSI Design
10EC662 – Satellite Communications
10EC663 - Random Process

10EC664 – Low Power VLSI Design
10EC665 – Data Structure Using C++
10EC666 – Digital System Design Using Verilog
10EC667- Virtual Instrumentation

2

5

**SCHEME OF TEACHING AND EXAMINATION
B.E. ELECTRONICS AND COMMUNICATION**

VII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC71	Computer Communication Networks	EC	4	-	3	25	100	125
2	10EC72	Optical Fiber Communication	EC	4	-	3	25	100	125
3	10EC73	Power Electronics	EC	4	-	3	25	100	125
4	10EC74	Embedded System Design	EC	4	-	3	25	100	125
5	10EC75x	Elective-II (Group B)	EC	4	-	3	25	100	125
6	10EC76x	Elective-III (Group C)	EC	4	-	3	25	100	125
7	10ECL77	VLSI Lab	EC	-	3	3	25	50	75
8	10ECL78	Power Electronics Lab	EC	-	3	3	25	50	75
TOTAL				24	06	24	200	700	900

Elective-II (Group B)

Elective-III (Group C)

10EC751 – DSP Algorithms & Architecture
10EC752 - Micro and Smart Systems Technology
10EC753 – Artificial Neural Network
10EC754 – CAD for VLSI
10EC755 – Applied Embedded System Design*
10EC756 – Speech Processing

10EC761 - Programming in C++
10EC762 – Real Time Systems
10EC763 - Image Processing
10EC764 - Radio Frequency Integrated Circuits
10EC765 - Wavelet Transforms
10EC766 - Modeling and Simulation of Data Networks

NOTE: 10EC755 Applied Embedded System Design has a LAB component (syllabus is different and in the Theory Examination, questions from Lab experiments will also be there.)

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VIII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC81	Wireless Communication	EC	4	-	3	25	100	125
2	10EC82	Digital Switching Systems	EC	4	-	3	25	100	125
3	10EC83x	Elective-IV (Group D)	EC	4	-	3	25	100	125
4	10EC84x	Elective-V (Group E)	EC	4	-	3	25	100	125
5	10ECP85	Project Work	EC	-	6	3	100	100	200
6	10ECS86	Seminar	EC	-	3	-	50	-	50
TOTAL				16	09	15	250	500	750

Elective-IV (Group-D)

10EC831 – Distributed Systems

10EC832 – Network Security

Elective-V (Group-E)

10EE841 – Multimedia Communication

10EC842 – Real Time Operating Systems

10EC833 - Optical Networks
10EC834 - High Performance Computing Networks
10EC835 - Internet Engineering

10EC843 - GSM
10EC844 - Ad-hoc Wireless Networks
10EC845 - Optical Computing

